

QEVA

Qseven Evaluation Backplane

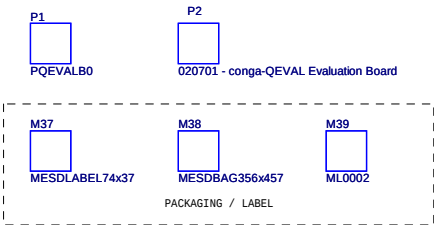
Rev. B.0

Content

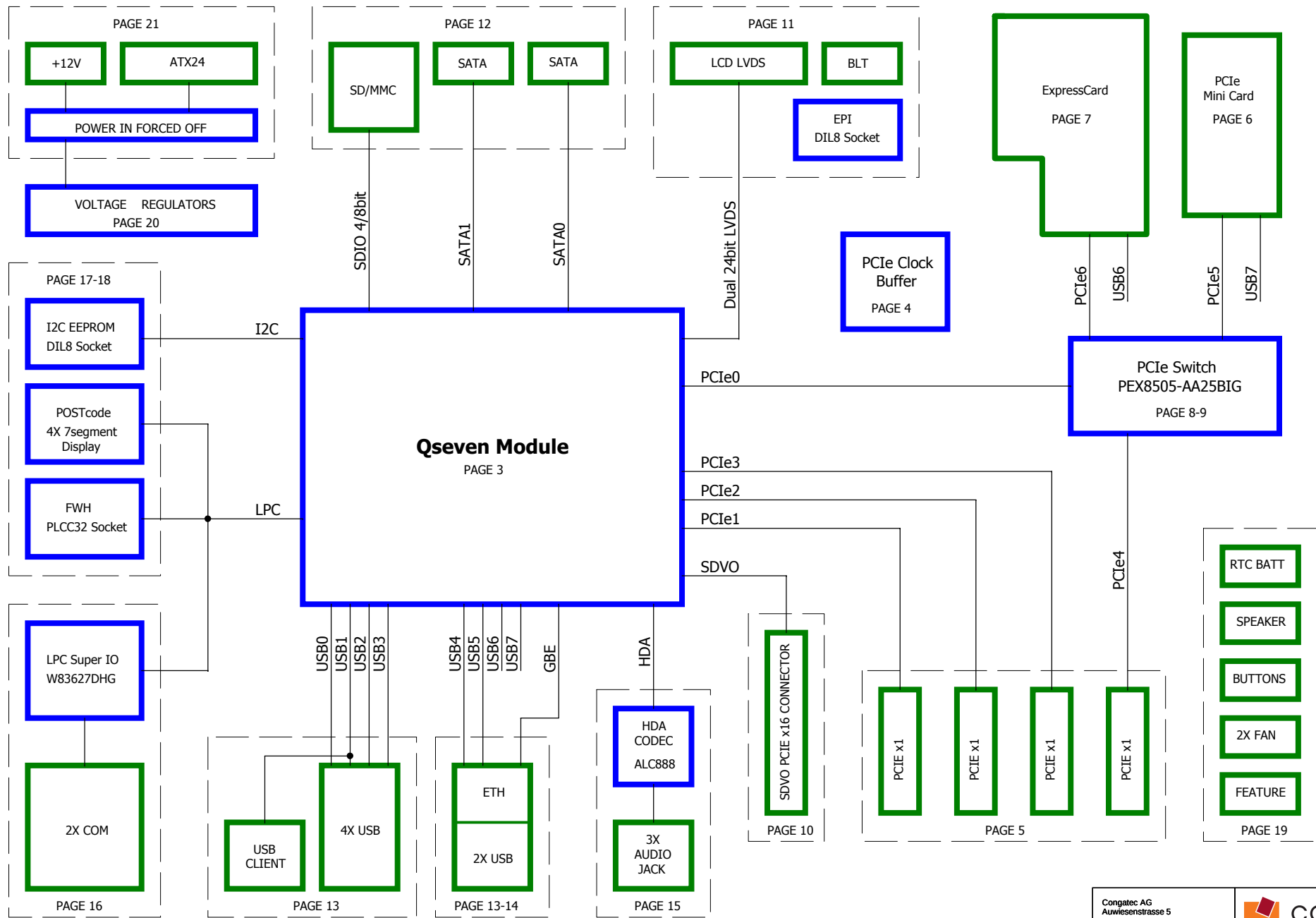
Page 1	Cover Sheet
Page 2	Block Diagram
Page 3	Qseven Connector
Page 4	PCIe Clock / Misc.
Page 5	PCIe x1 Slots
Page 6	PCIe Mini Card
Page 7	ExpressCard
Page 8	PCIe Switch
Page 9	PCIe Switch Cfg / Pwr
Page 10	SDVO Connector
Page 11	LCD LVDS Connector
Page 12	SATA / SDIO
Page 13	USB Ports
Page 14	Ethernet / HDA/AC97 CARD EDGE CONNECTOR
Page 15	HD Audio
Page 16	COM Ports
Page 17	FWH / SPI / I2C EEPROM / SMB EEPROM
Page 18	CAN / POSTcode
Page 19	BATT / SPKR / Feature / FAN
Page 20	Power 5.5V_ALW / 3.3V_ALW / 5V / 3.3V / 1.5V
Page 21	ATX24 / +12V POWER IN
Page 22	Revision History

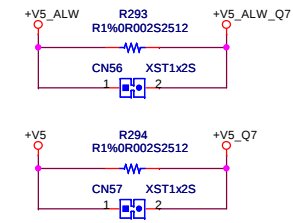
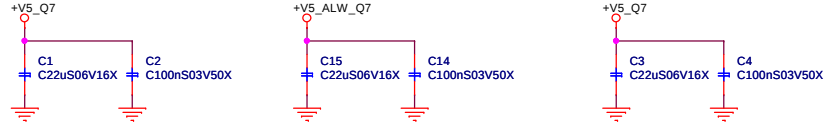
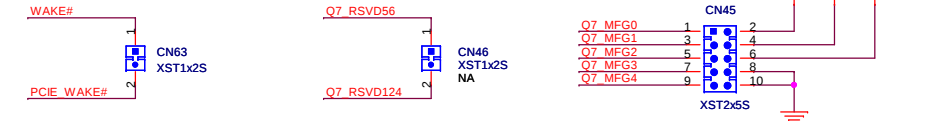
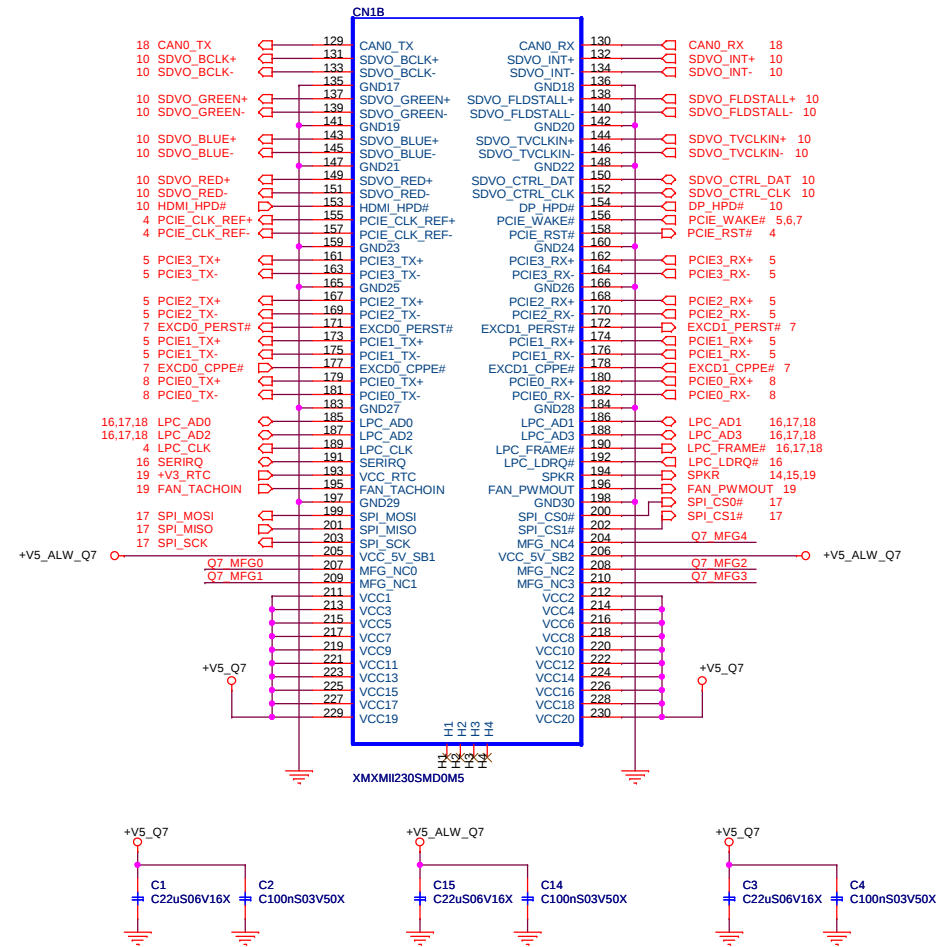
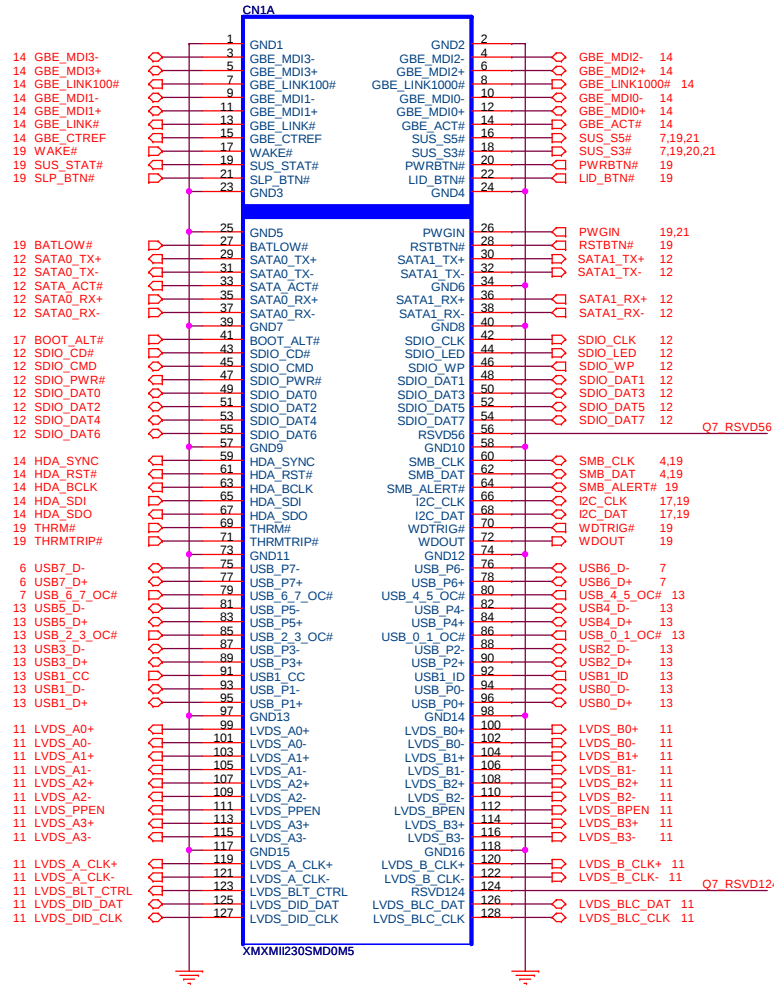
Variants

Base Components used on all options



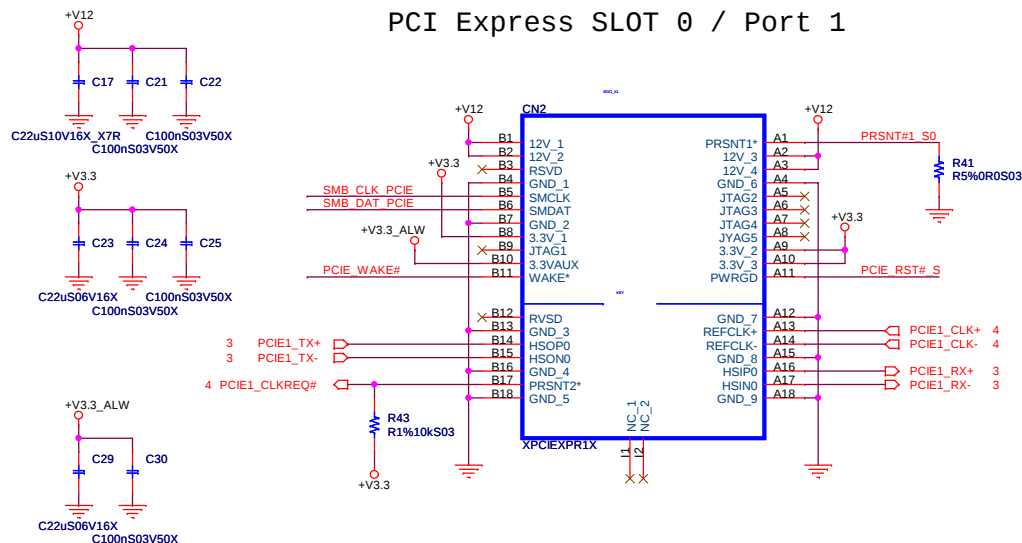
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Title QEVA - Qseven Evaluation Backplane			
Size A3	Document Number QEVASB0	Created	Rev B.0
Date:	Monday, February 21, 2011	Sheet 1	of 22



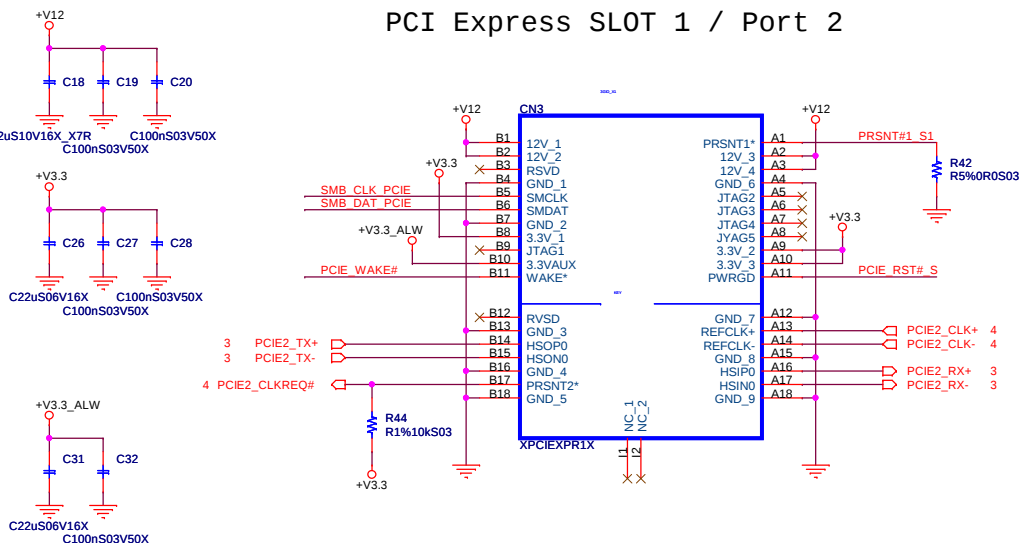


4,6,7 SMB_CLK_PCIE
4,6,7 SMB_DAT_PCIE
3,6,7 PCIE_WAKE#
4,6,7,10 PCIE_RST#_S

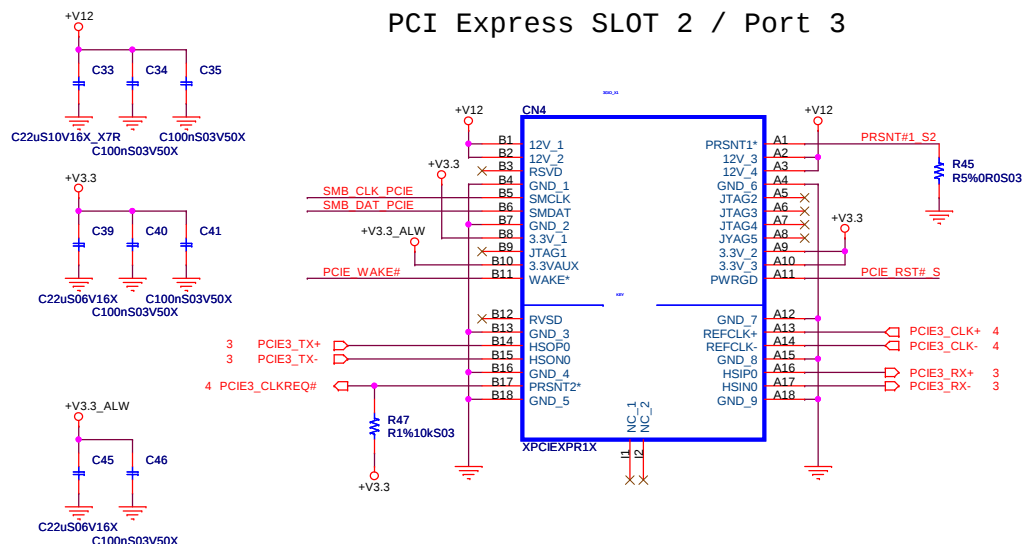
PCI Express SLOT 0 / Port 1



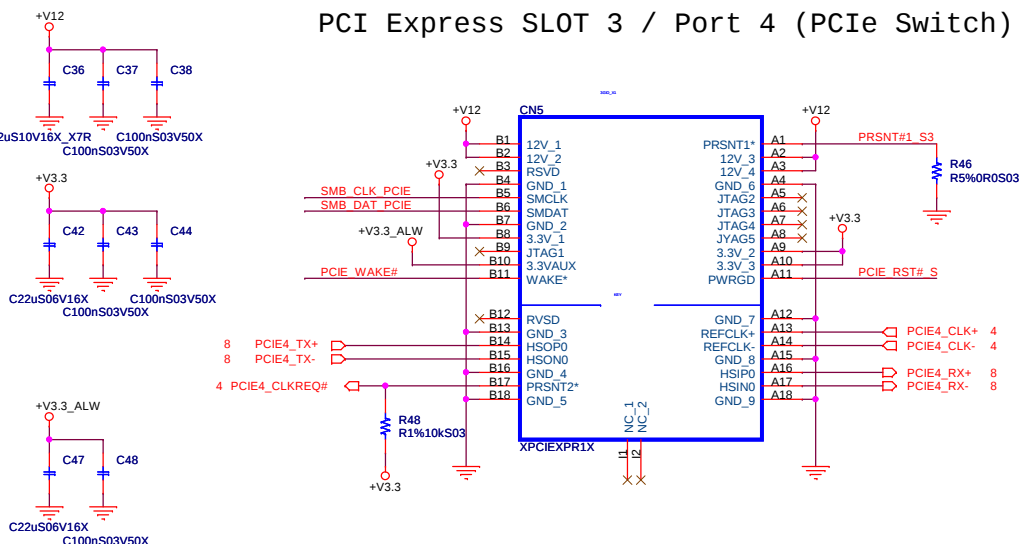
PCI Express SLOT 1 / Port 2



PCI Express SLOT 2 / Port 3



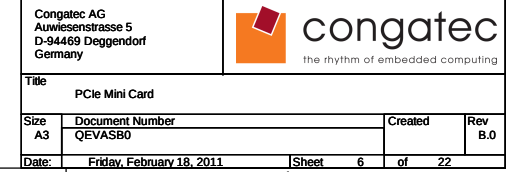
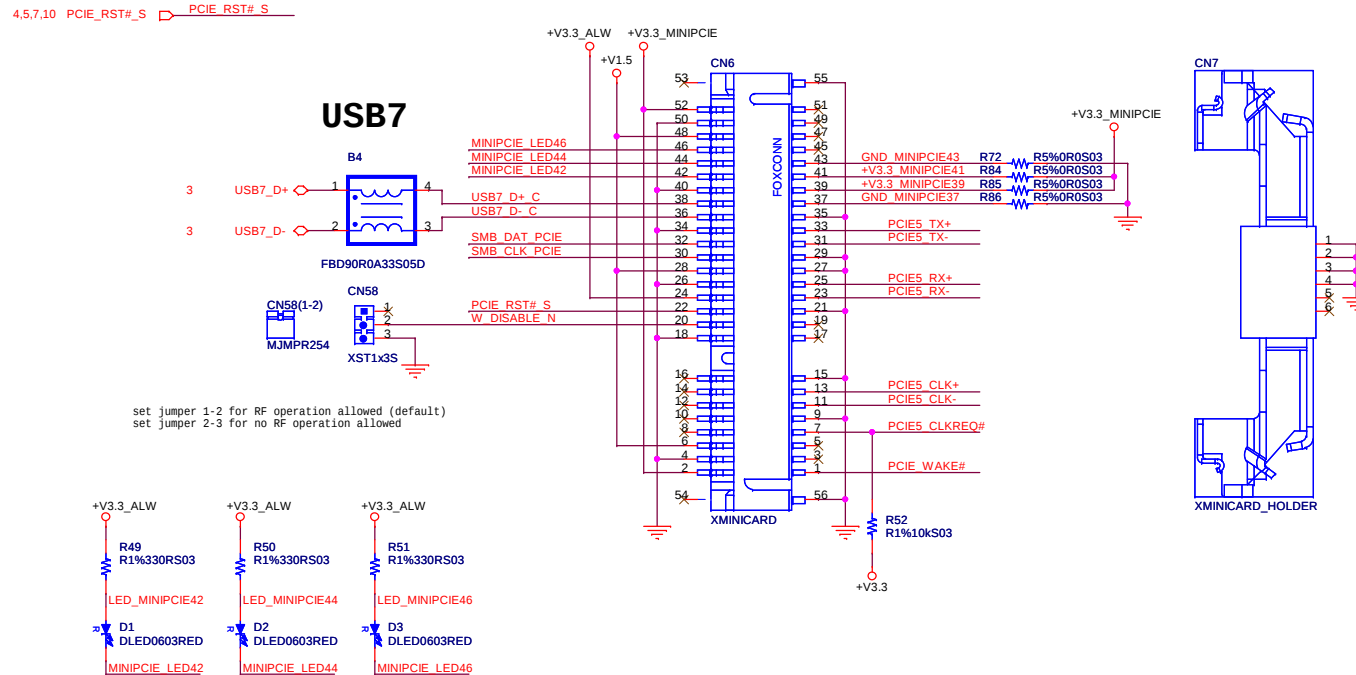
PCI Express SLOT 3 / Port 4 (PCIe Switch)

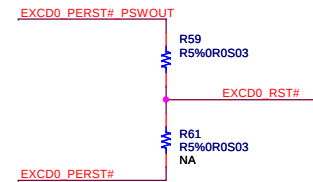
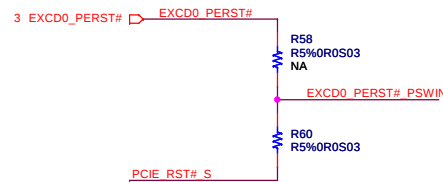
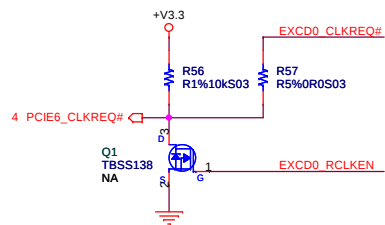
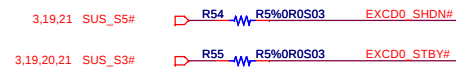
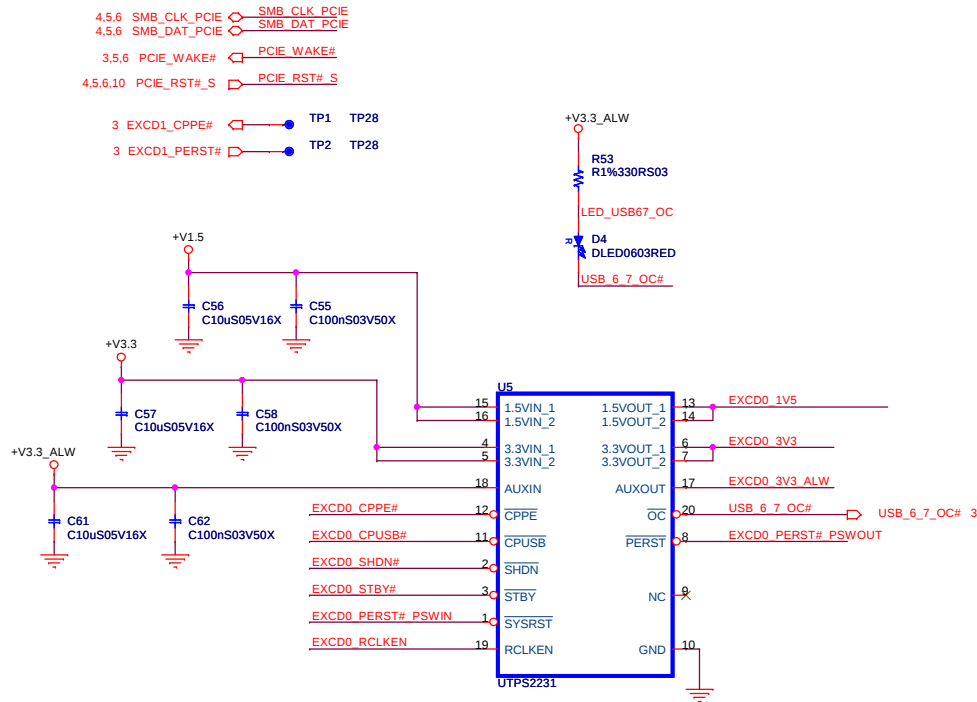


Congatec AG
Auwiesenstrasse 5
D-94469 Deggendorf
Germany



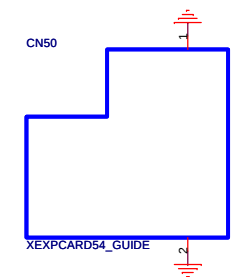
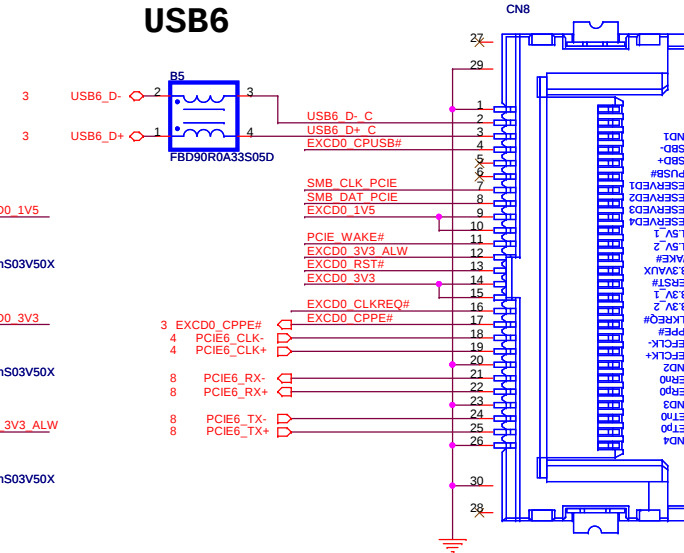
Title PCIe x1 Slots			
Size A3	Document Number QEVASB0	Created	Rev B.0
Date: Friday, February 18, 2011	Sheet 5	of 22	





ExpressCard / Port 6 (PCIe Switch)

USB6

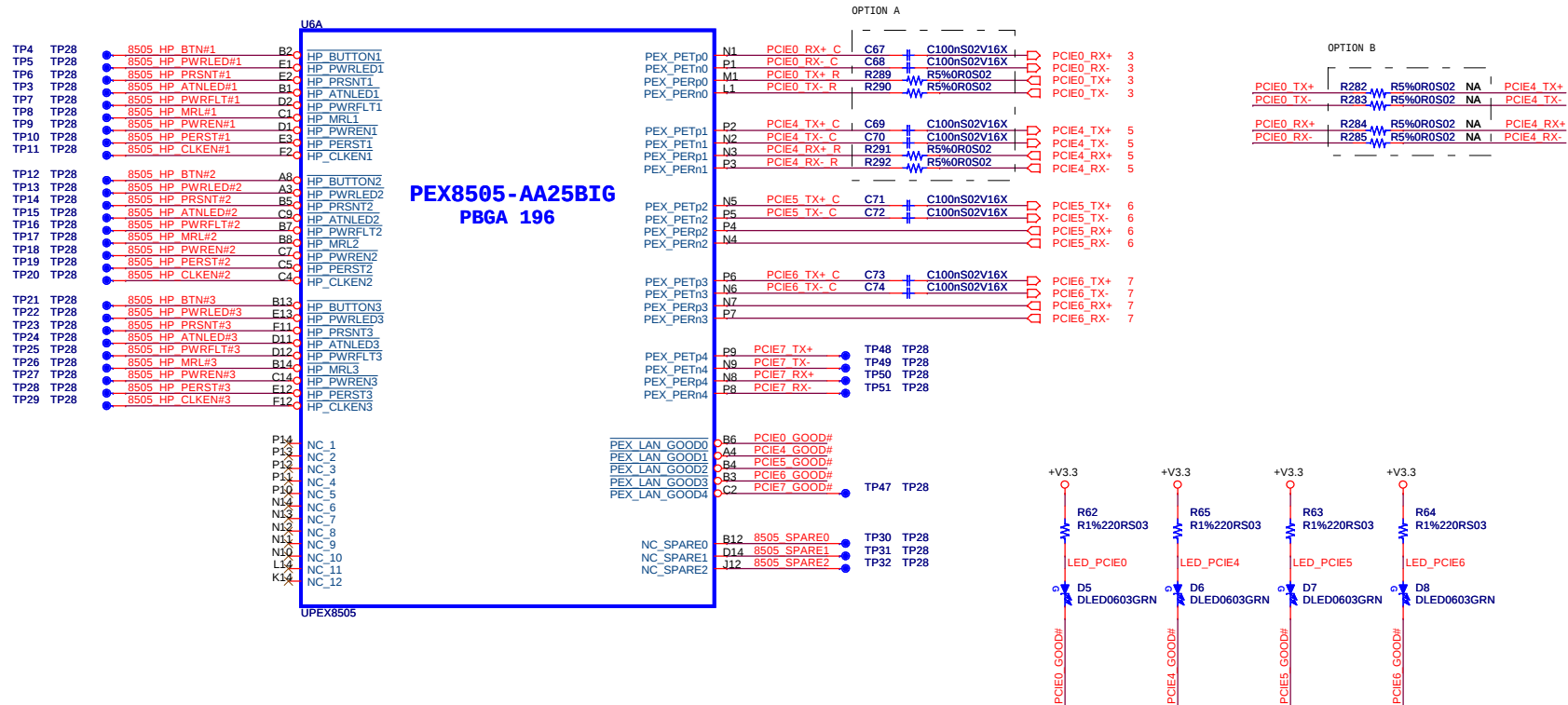


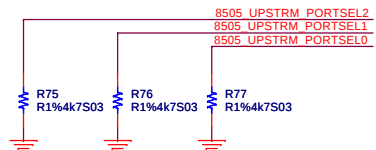
Congatec AG
Auwiesenstrasse 5
D-94469 Deggendorf
Germany



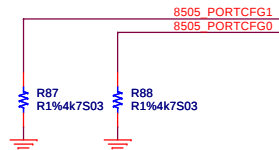
Title ExpressCard			
Size A3	Document Number QEVASB0	Created	Rev B.0
Date: Friday, February 18, 2011	Sheet 7	of 22	

Optional Qseven PCIe0 port / PCIE SLOT 4 connection:
OPTION A: Qseven PCIe0 port connected to PCIE SWITCH
PCIE SLOT 4 connected to PCIE SWITCH
OPTION B: Qseven PCIe0 port connected to PCIE SLOT 4

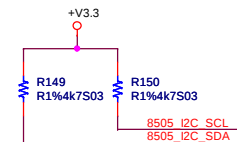




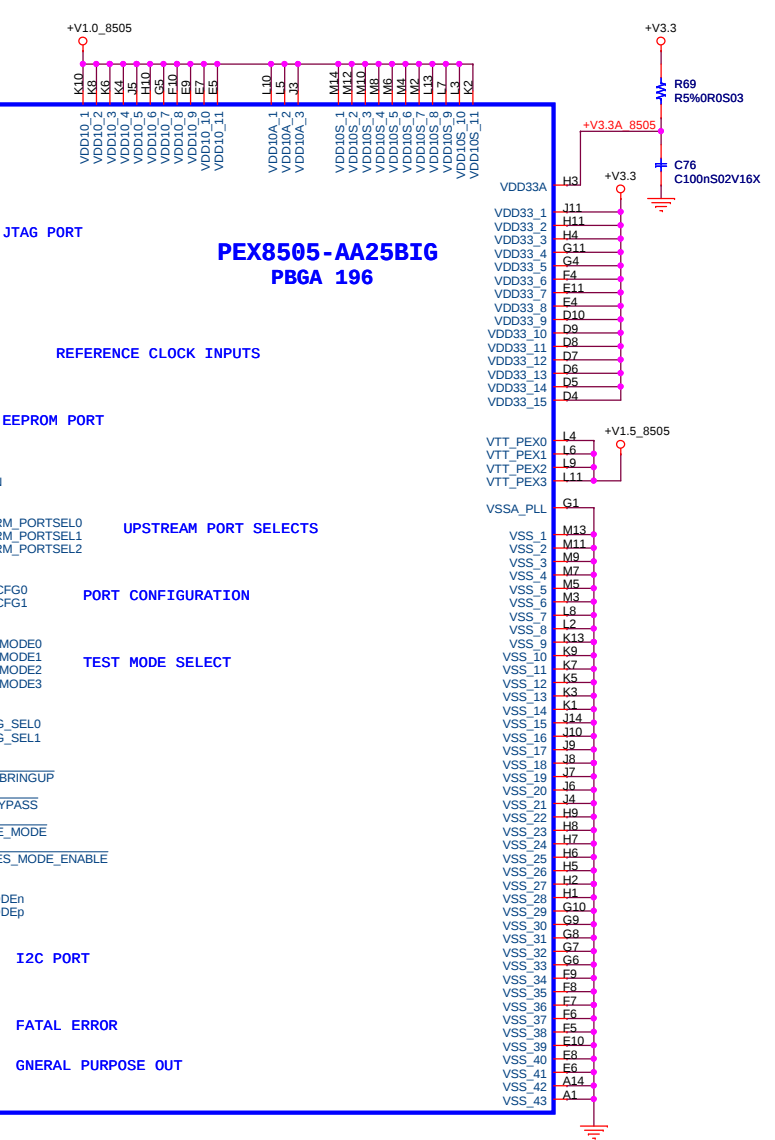
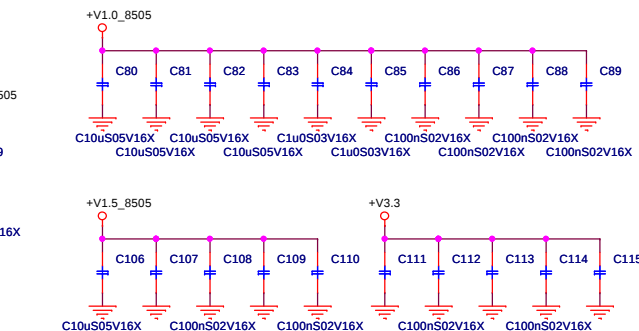
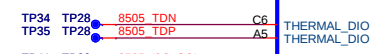
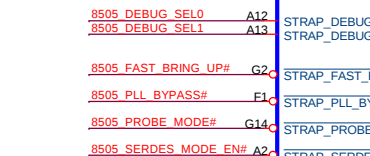
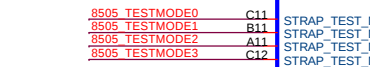
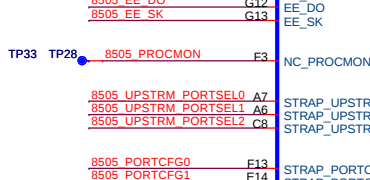
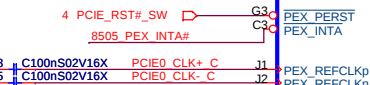
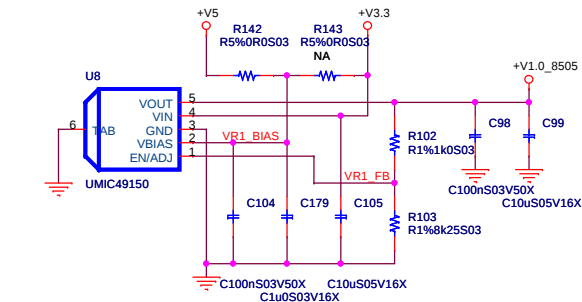
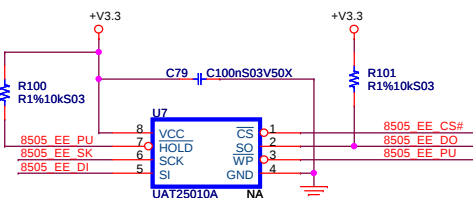
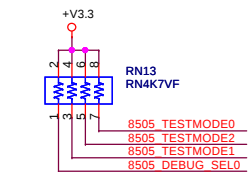
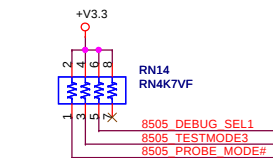
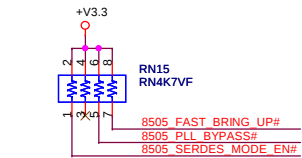
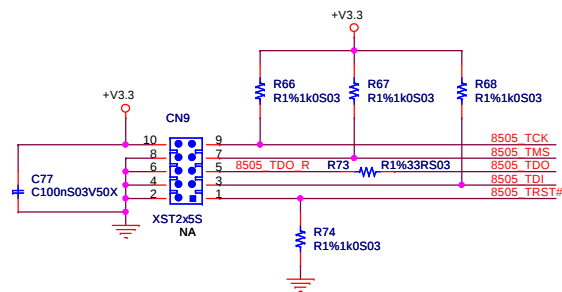
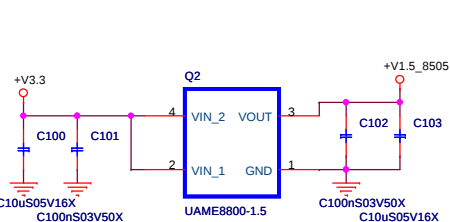
UP STREAM PORT= PORT0 = LLL



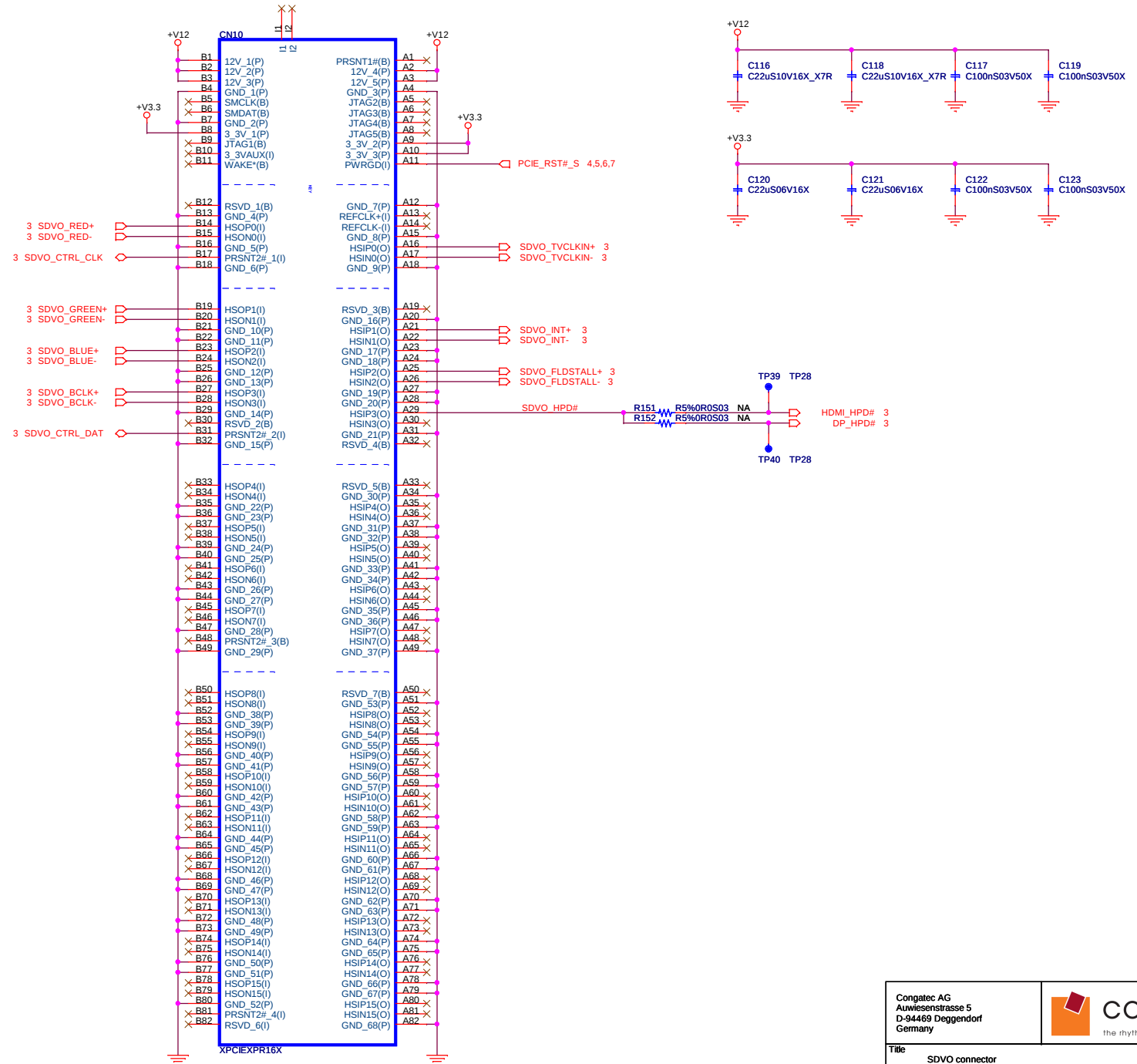
Port Configuration for Station 0
= x1,x1,x1,x1,x1



I2C ADDRESS = 0x70



NOTE: Supports only ADD2-N cards in this slot. ADD2-R cards are not supported.

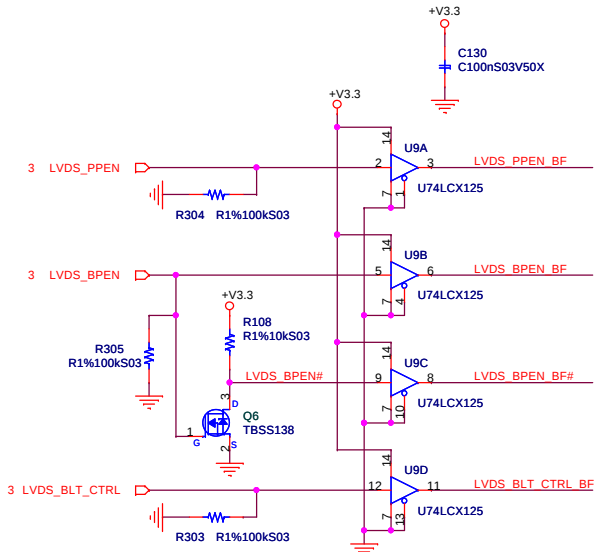




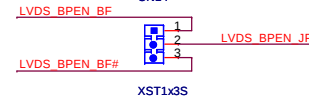
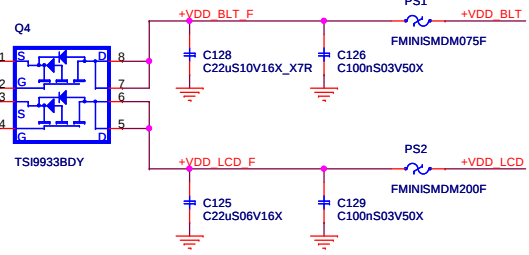
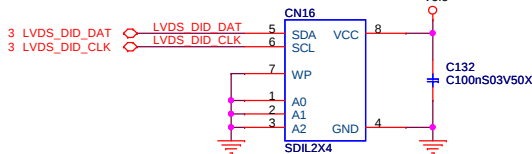
set jumper 1-2 for 12V backlight voltage (default)
set jumper 2-3 for 5V backlight voltage



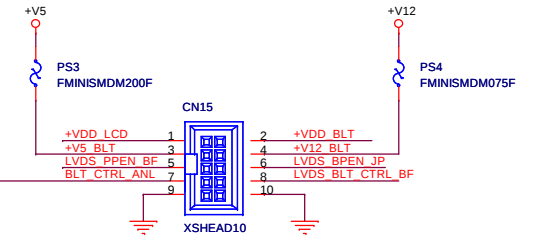
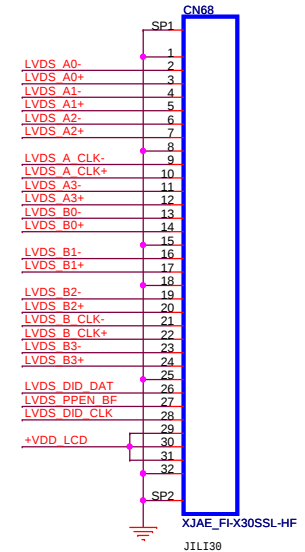
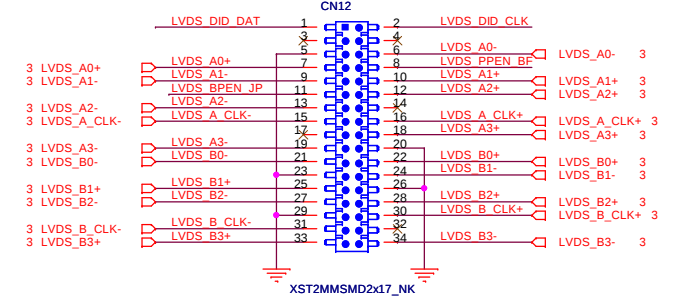
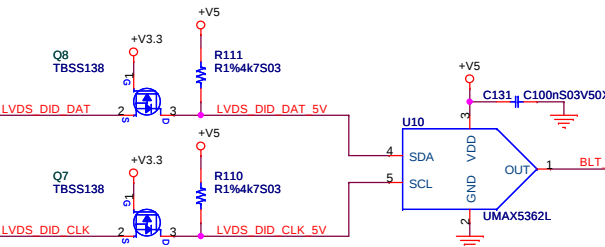
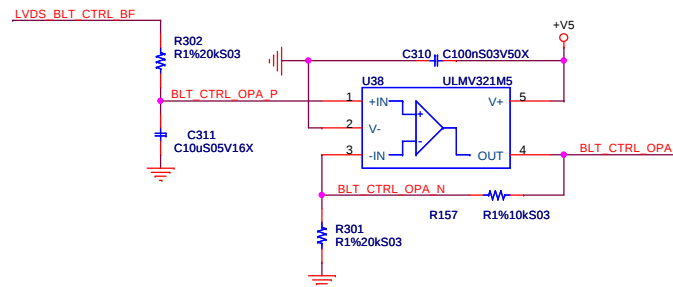
set jumper 1-2 for 3.3V LCD voltage
set jumper 2-3 for 5V LCD voltage (default)



3 LVDS_BLC_DAT TP45 TP28
3 LVDS_BLC_CLK TP46 TP28

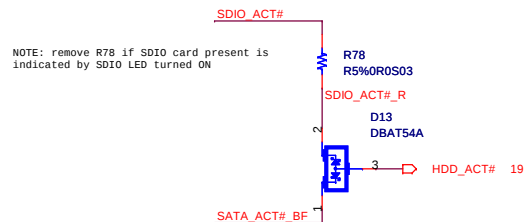
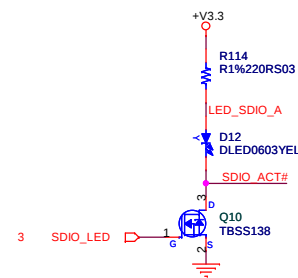
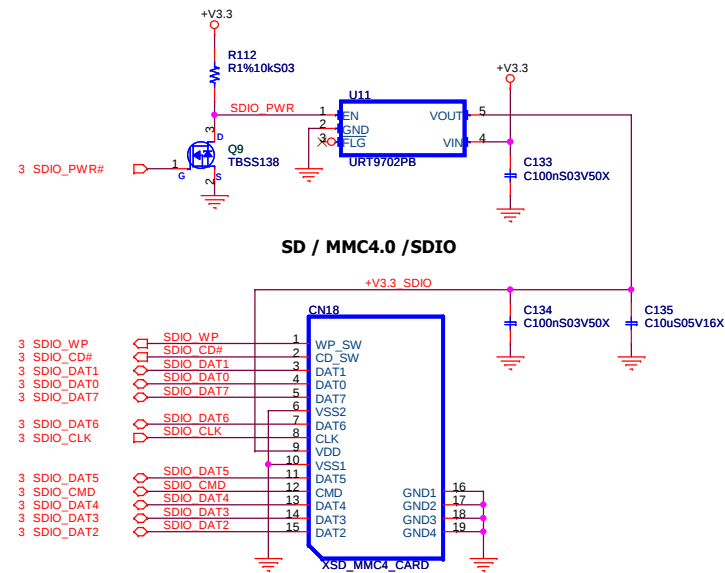
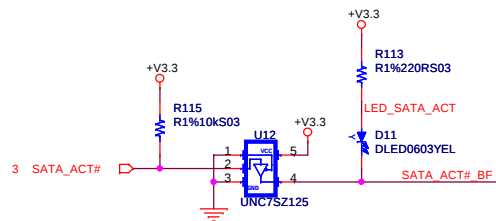
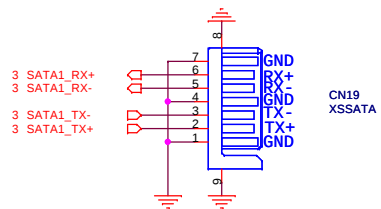


set jumper 1-2 for Backlight enable HIGH active (default)
set jumper 2-3 for Backlight enable LOW active



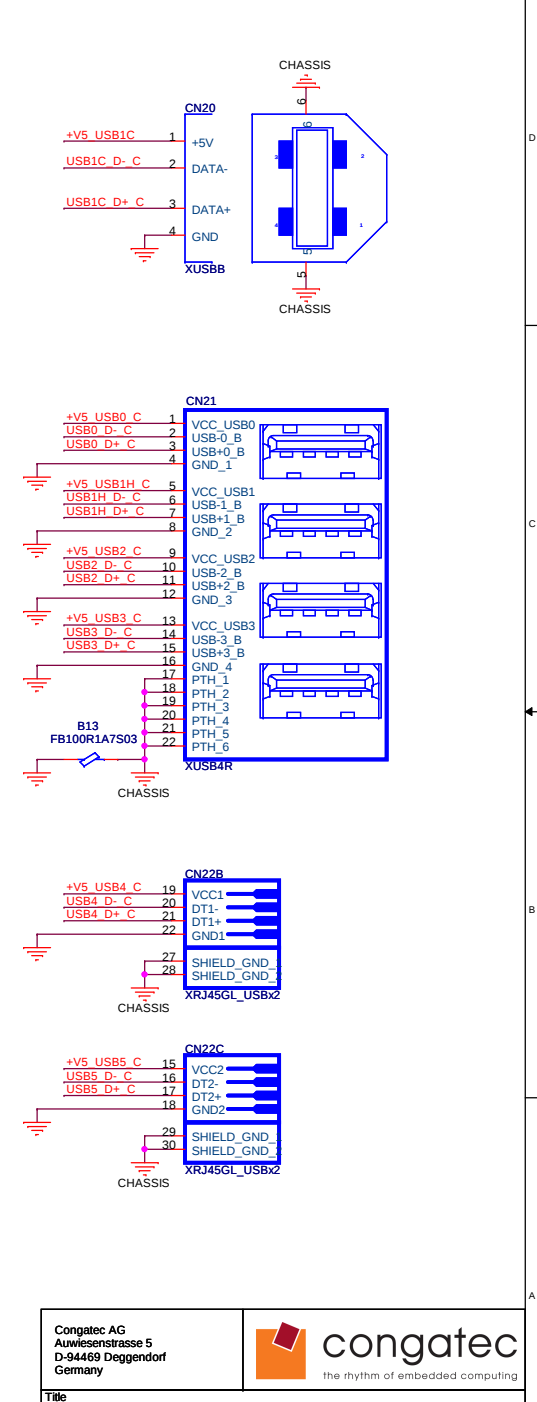
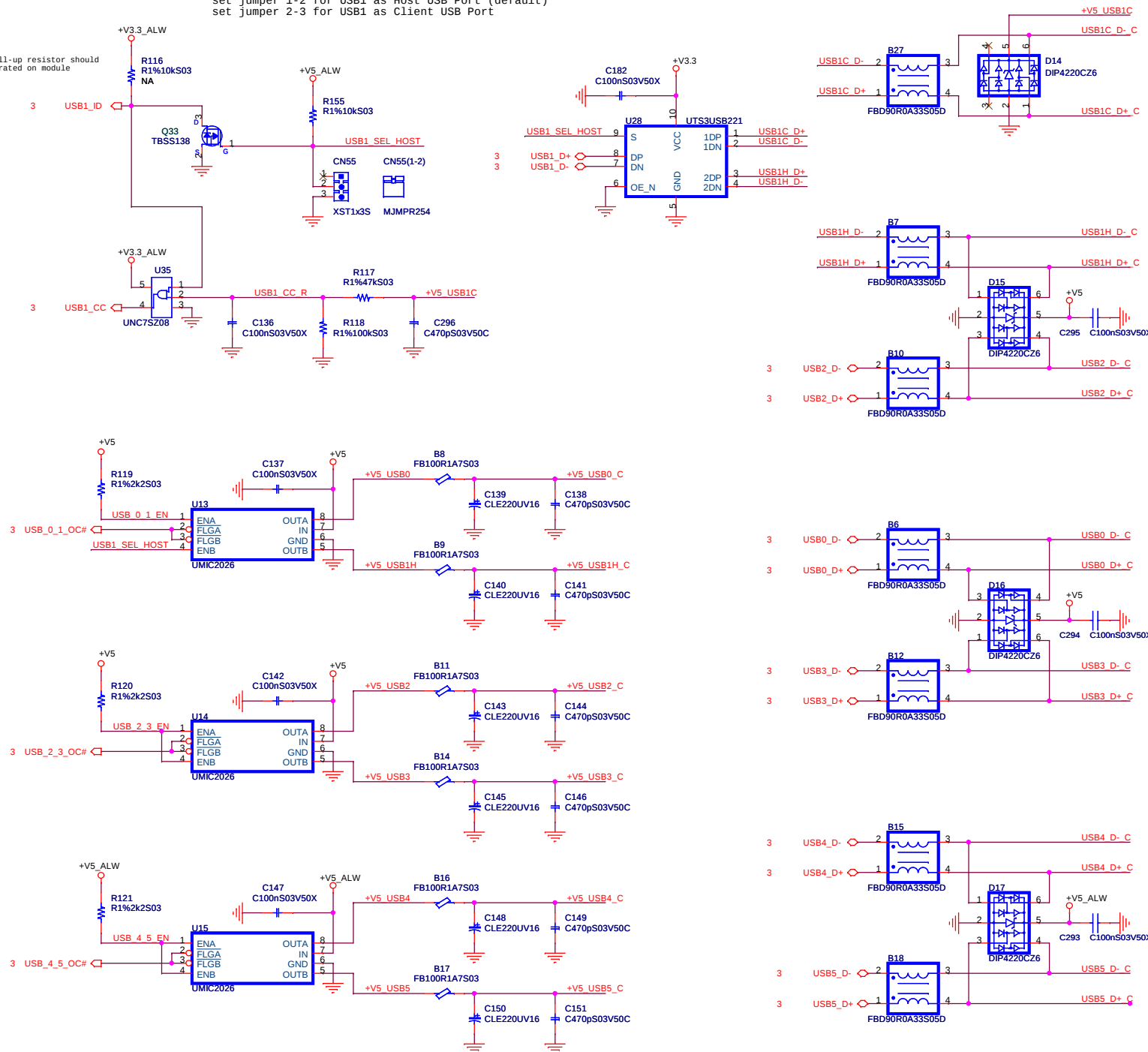
set jumper 1-2 for Backlight control by PWM to ANALOG
set jumper 2-3 for Backlight control by I2C DAC (default)

Congatec AG Auwiesenstrasse 5 D-94469 Deggendorf Germany				congatec the rhythm of embedded computing	
Title LCD LVDS connector					
Size A3	Document Number QEVASB0			Created	Rev B.0
Date:	Friday, February 18, 2011		Sheet	11	of 22

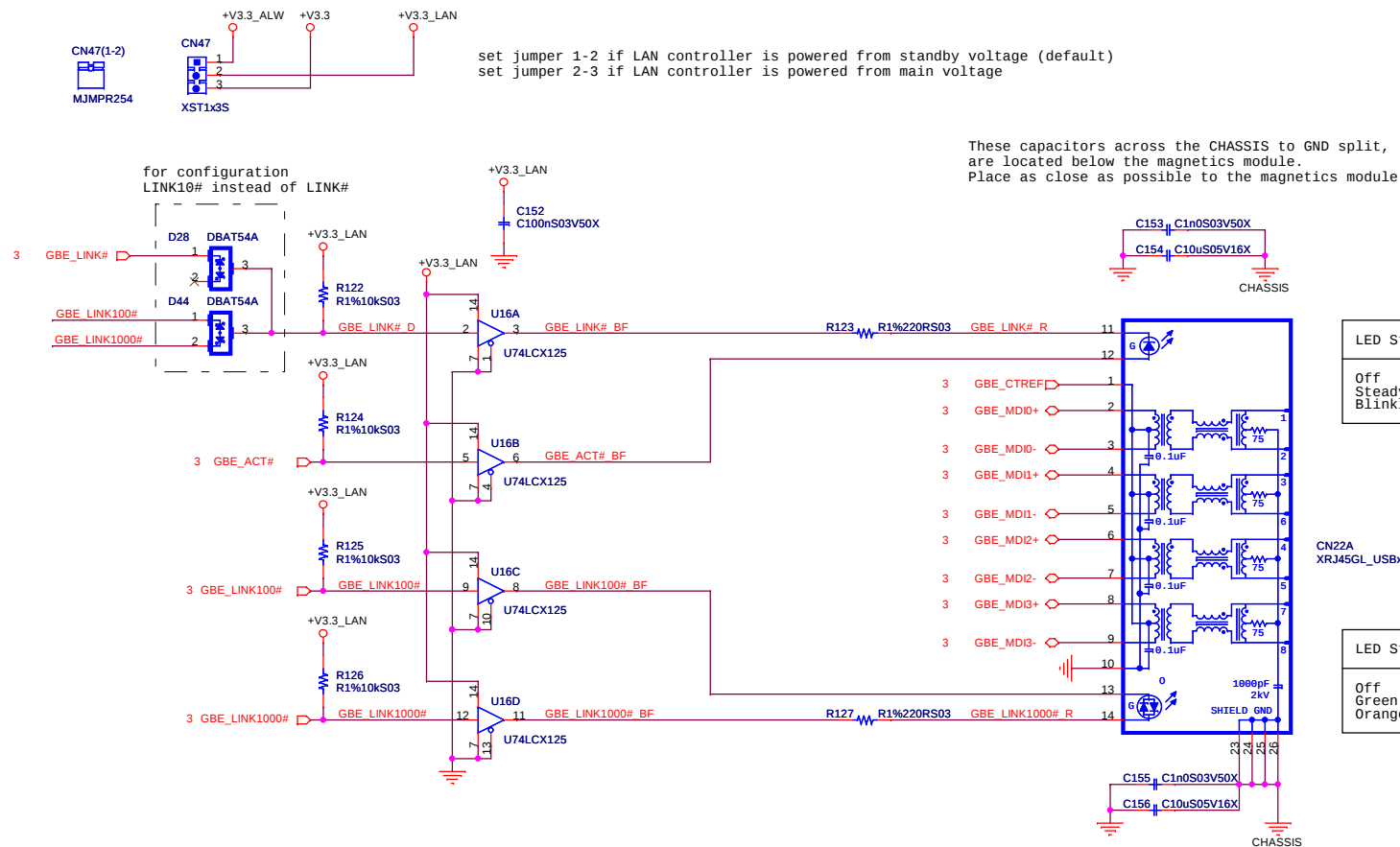


set jumper 1-2 for USB1 as Host USB Port (default)
set jumper 2-3 for USB1 as Client USB Port

Note: pull-up resistor should be integrated on module



GIGABIT ETHERNET

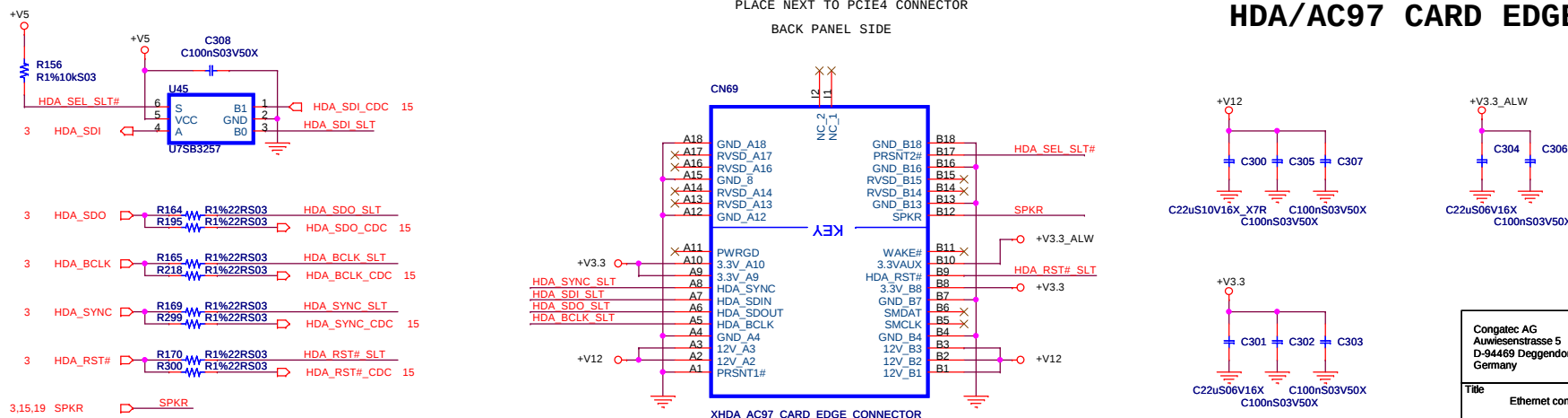


LED State	Description
Off	No Link
Steady On	Link established, no activity detected
Blinking	Link established, activity detected

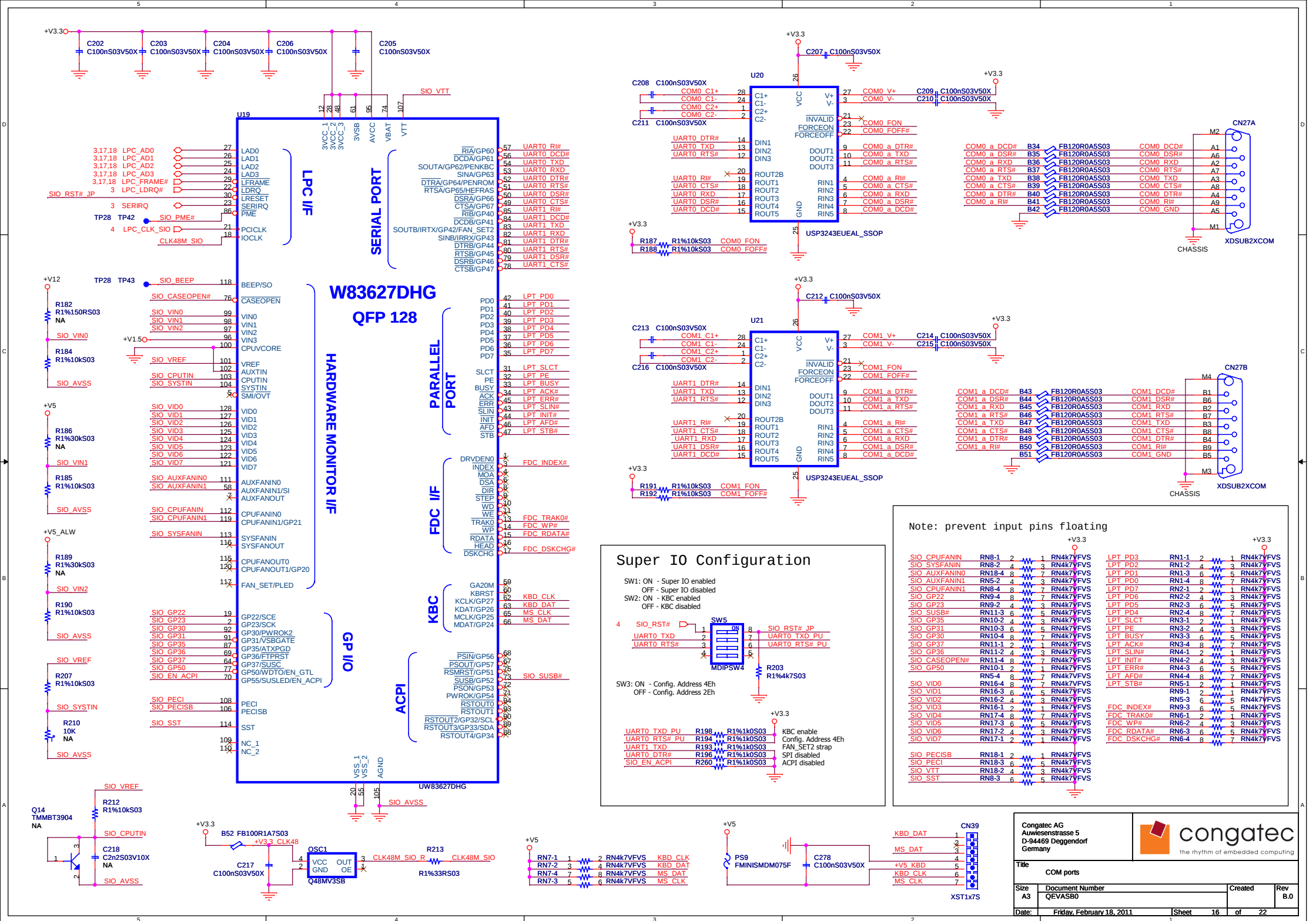
LED State	Description
Off	10 Mbps link speed
Green	100 Mbps link speed
Orange	1000 Mbps link speed

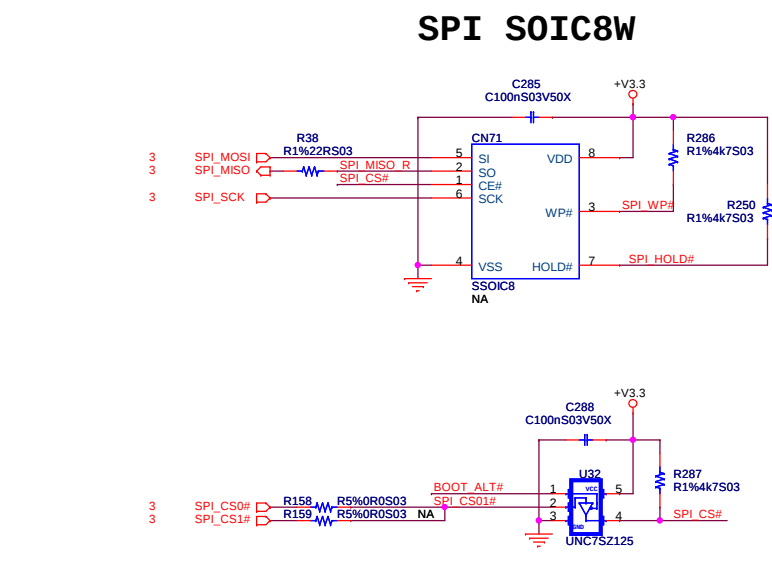
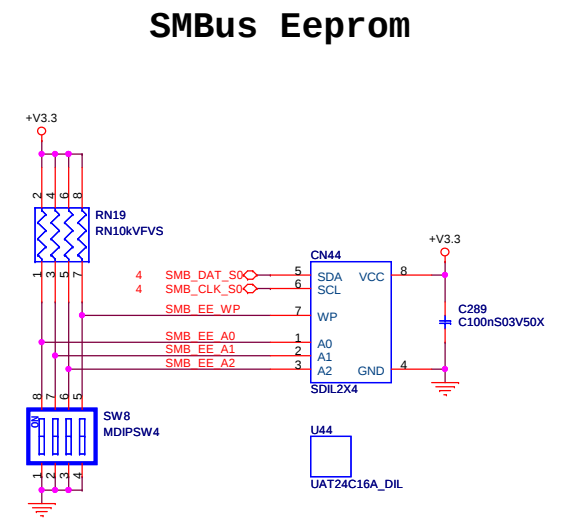
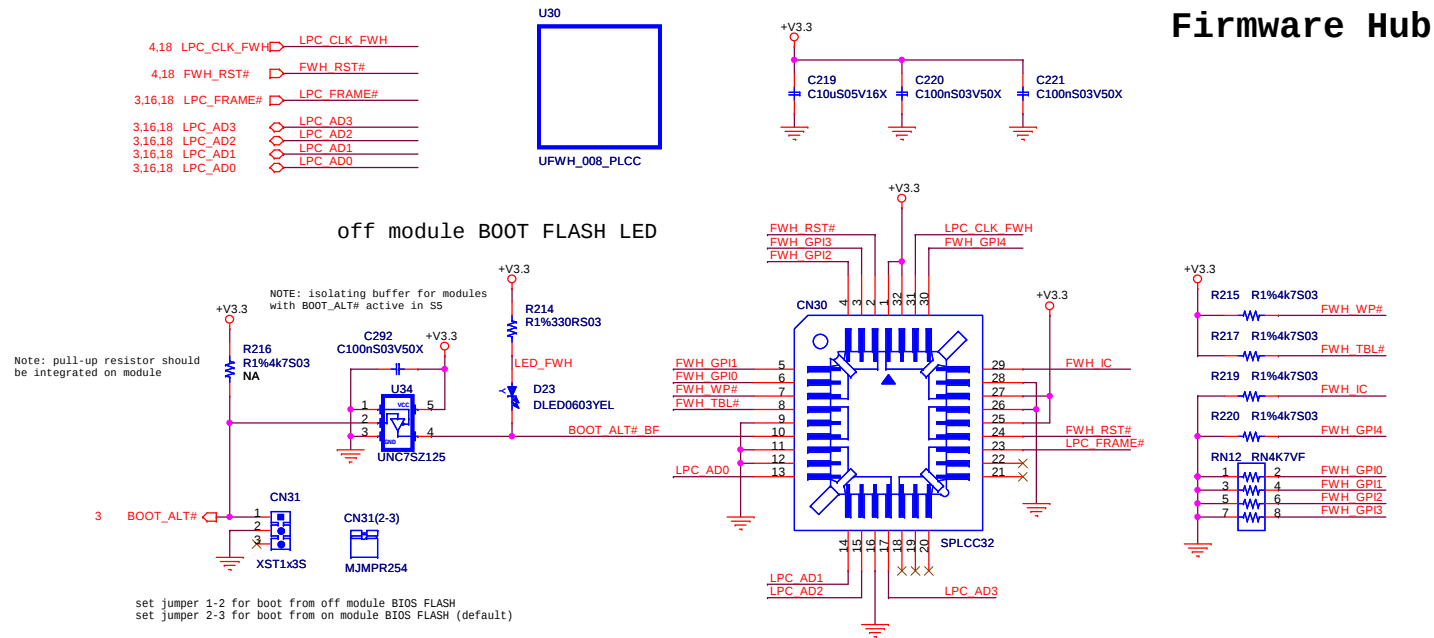
PLACE NEXT TO PCIE4 CONNECTOR
BACK PANEL SIDE

HDA/AC97 CARD EDGE CONNECTOR

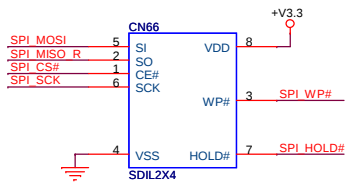


Congatec AG Auwiesenstrasse 5 D-94469 Deggendorf Germany				congatec the rhythm of embedded computing	
Title Ethernet connector / HDA/AC97 CARD EDGE CONNECTOR					
Size A3	Document Number QEVASB0			Created	Rev B.0
Date:	Friday, February 18, 2011	Sheet	14	of	22

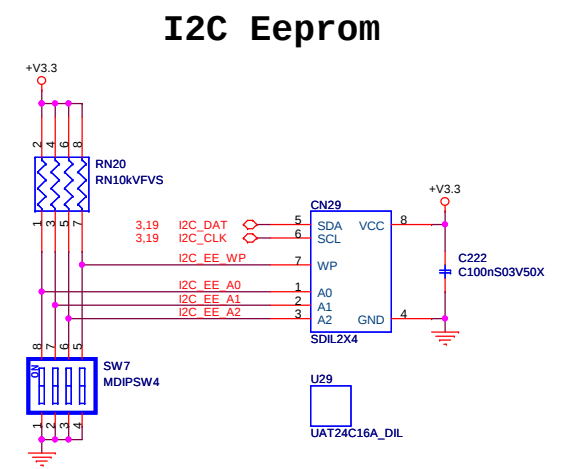
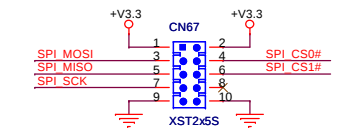




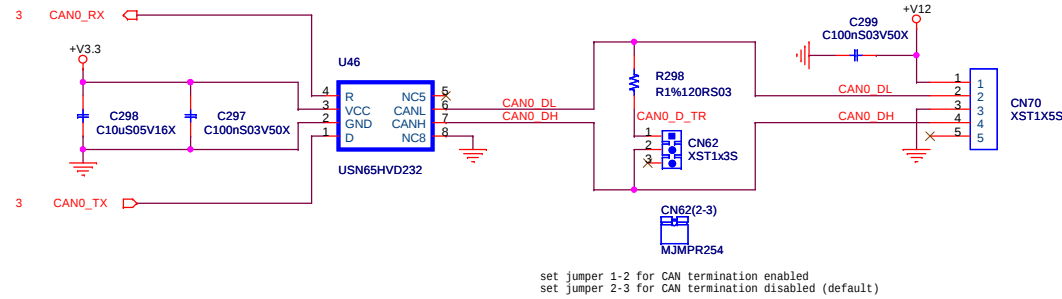
SPI DIP8 SOCKET



SPI HEADER

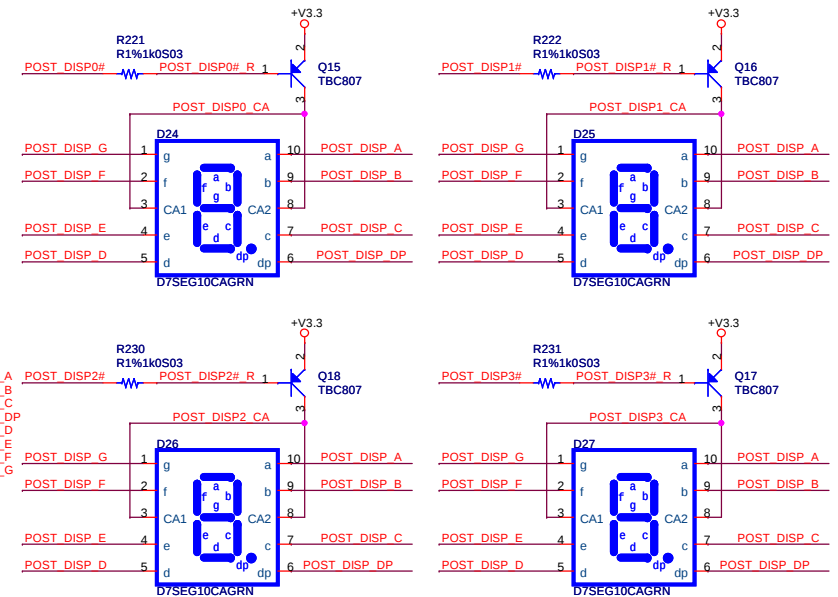
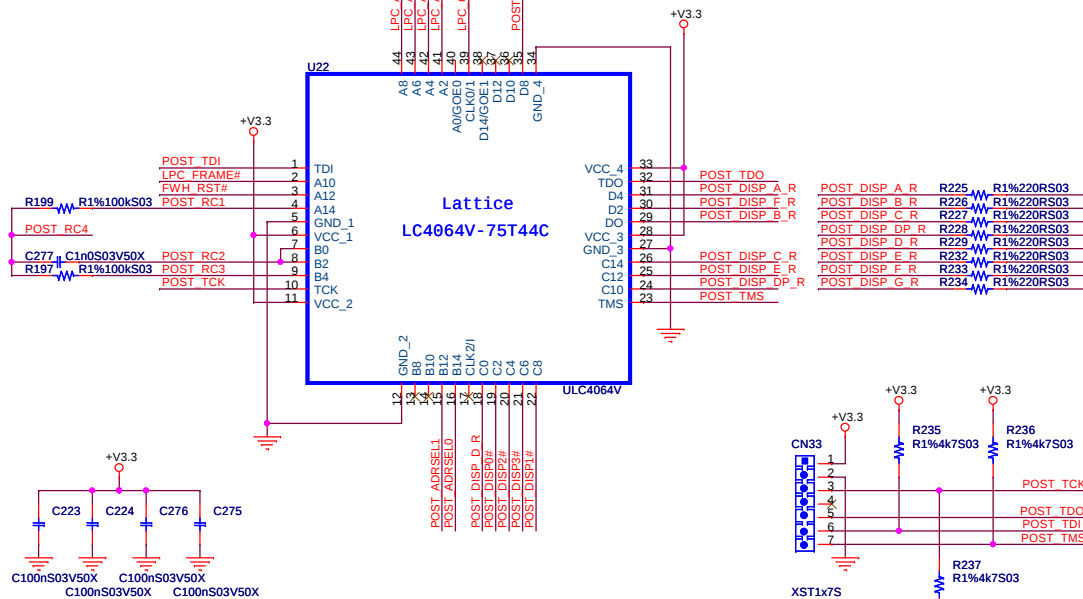


CAN TRANSCEIVER



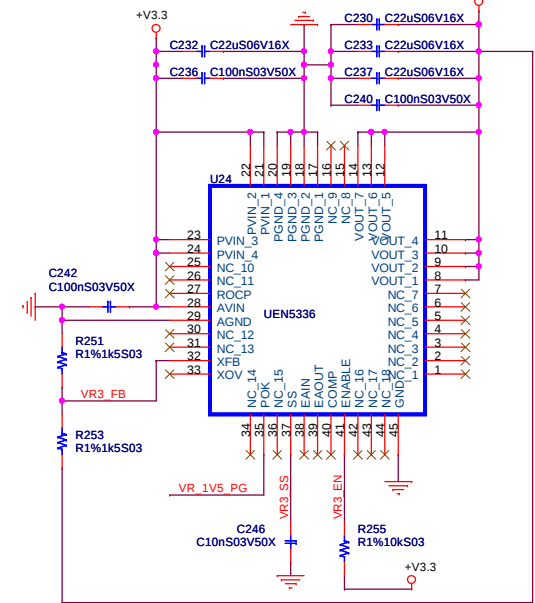
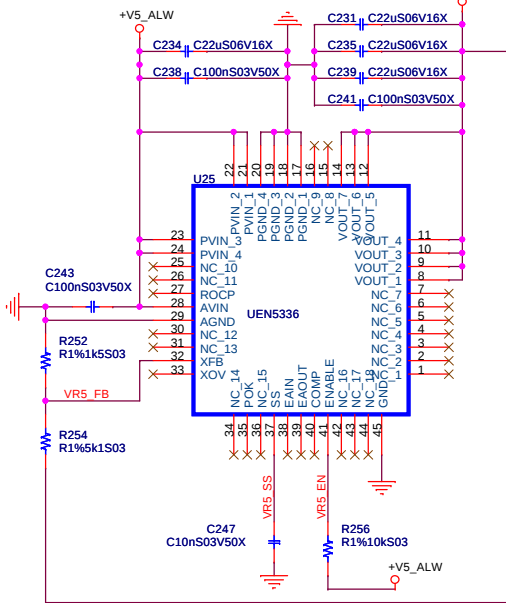
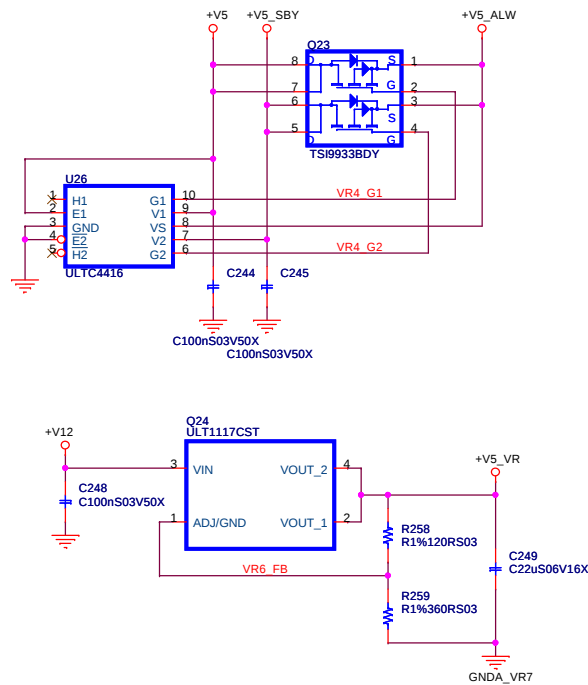
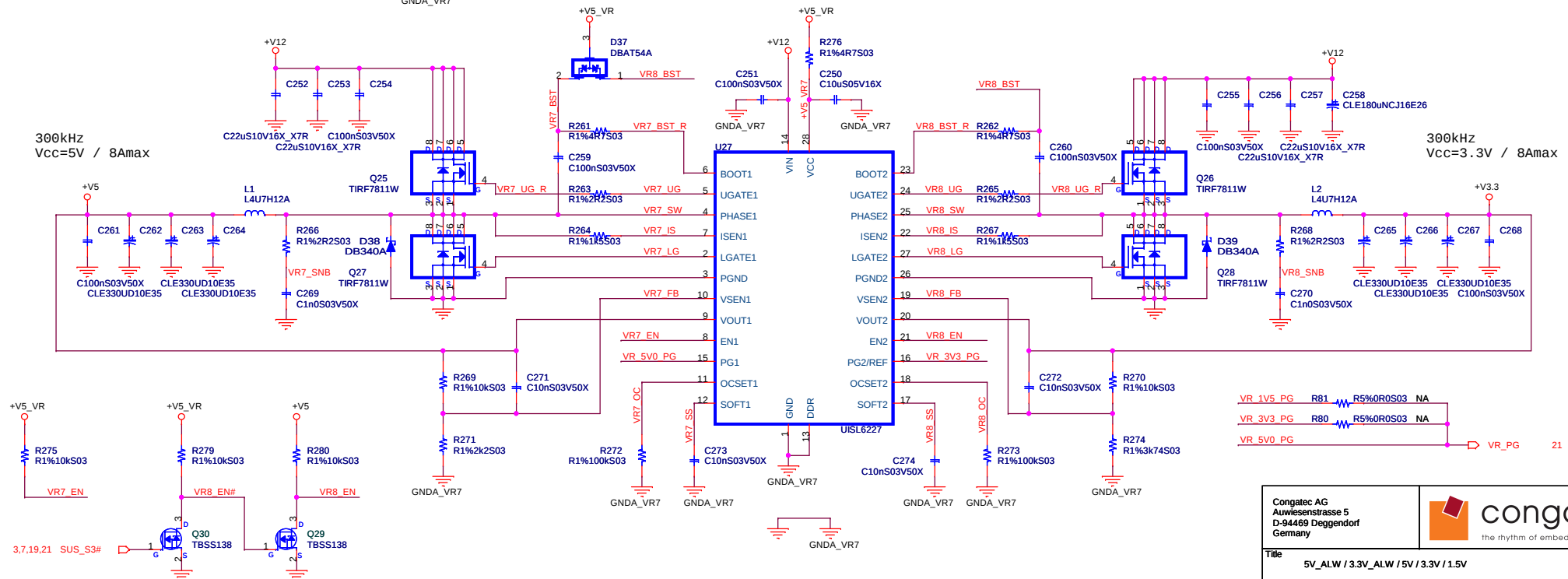
POSTcode

- 4.17 LPC_CLK_FWH# LPC CLK FWH
- 4.17 FWH_RST# FWH RST#
- 3.16,17 LPC_FRAME# LPC FRAME#
- 3.16,17 LPC_AD3 LPC AD3
- 3.16,17 LPC_AD2 LPC AD2
- 3.16,17 LPC_AD1 LPC AD1
- 3.16,17 LPC_AD0 LPC AD0

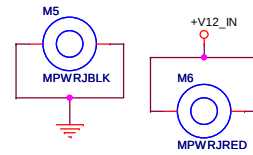


3.3V / 3Amax / 4.5Aoc

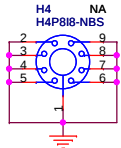
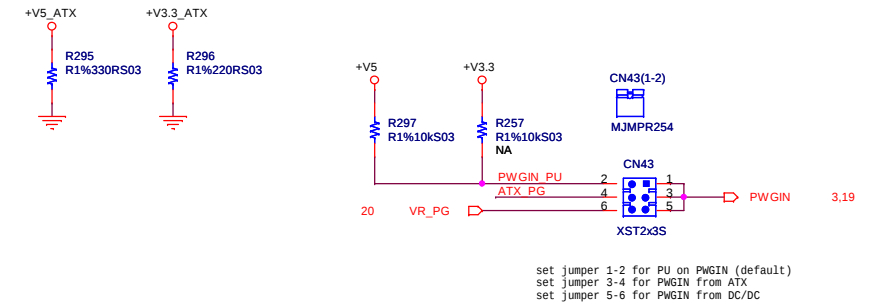
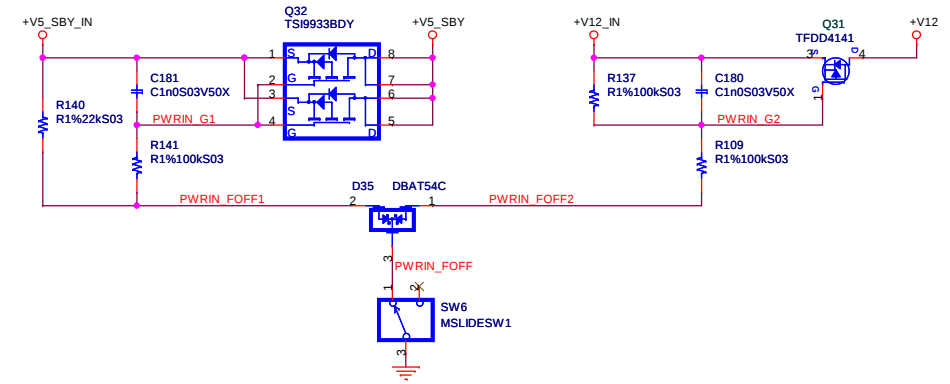
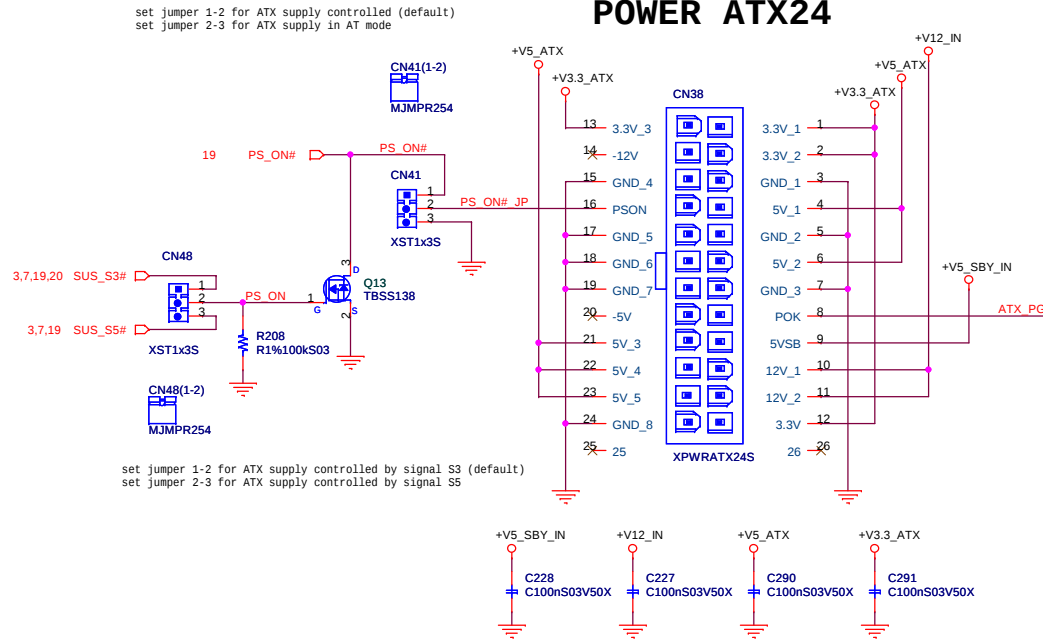
1.5V / 3Amax / 4.5Aoc

300kHz
VCC=5V / 8Amax

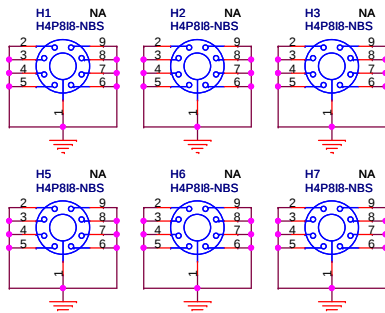
INPUT POWER VOLTAGE FORCED OFF



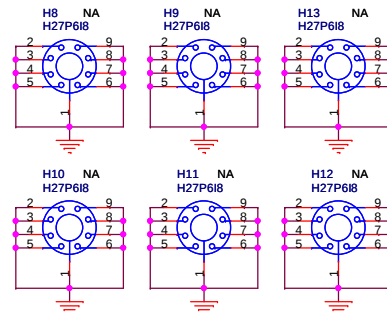
POWER ATX24



Backplane



Qseven



GND Probe

