

## congatec Application Note

<b>Affected Products</b>	All products
<b>Subject</b>	U.2 Introduction
<b>Confidential/Public</b>	Public
<b>Author</b>	GMX

## Revision History

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Revision	Date (yyyy-mm-dd)	Author	Changes
1.0	2020-05-04	GMX	Initial Revision

## Preface

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This Application Note introduces the SFF-8639 (U.2) interface. The goal is to give an overview of the technology and hint on how to design-in this technology for customer carrier boards.

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## Symbols

The following are symbols used in this application note.



***Notes call attention to important information that should be observed.***



***Cautions warn the user about how to prevent damage to hardware or loss of data.***



***Warnings indicate that personal injury can occur if the information is not observed.***

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## Terminology

Term	Description
NVMe	Non-Volatile Memory Express
SSD	Solid State Drive
SAS	Serial-Attached SCSI
SATA	Serial ATA
SFF	Small Form Factor
OD	Open-Drain

## 1 U.2 / SFF-8639 Introduction

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### 1.1 Introduction

The U.2 specification (initially SFF-8639), which is released by the PCI-SIG group, describes a hardware interface that supports several SSD interface technologies.

This specification is used mostly for connecting high performance NVMe SSDs. For overview of the NVMe protocol, visit [www.congatec.com](http://www.congatec.com) and view the technote “NVMe support on congatec COMs and SBCs” in the restricted area of the website.

To enable NVMe SSDs, the U.2 pinout features up to four PCIe lanes for inter-connection. Most often, this specification is used for SSDs in 2.5” form factor. This makes implementing the U.2 form factor a good method of scaling up the system speed while still being able to keep the same physical form factor for existing SATA-based SSDs.

In addition to PCIe-based SSDs, the interface also supports SAS and SATA. This enables the connector to work with different SSD technologies.

This application note is concerned with U.2 specification pinout as well as topologies that can be used to design-in the interface.

### 1.2 Backward Compatibility

One of the advantages of this specification is that it allows SATA, SAS and U.2 NVMe SSDs to be used on the same U.2 connector. This means that systems can be equipped with different types of SSD, depending on the demands of the system. In that way, the system can be scaled to perform different tasks, thereby improving the overall price efficiency of the design.

### 1.3 Advantages

U.2 SSDs are often used in data centers and server-related ecosystems. The SSD housing of a U.2 SSD is bigger and more ruggedized when compared to the bare PCB M.2 SSDs. This also allows U.2 devices to feature a bigger overall capacity.

U.2 devices can support hot-plug and hot-swap features. This capability allows new devices to be connected to the platform (hot-plug) or devices of the same type can be swapped out (hot-swap) while the system is up and running. The following conditions need to be met to support these features:

1. The target SSD needs to support unplanned power loss.
2. The BIOS of the congatec module you want to use needs PCIe hot-plug support.

U.2 SSDs often feature advanced power loss protection. This means that in a surprise power loss event, mandatory data transactions that have been cached can still be completed, making the SSD more robust to corruption in unstable power environments.



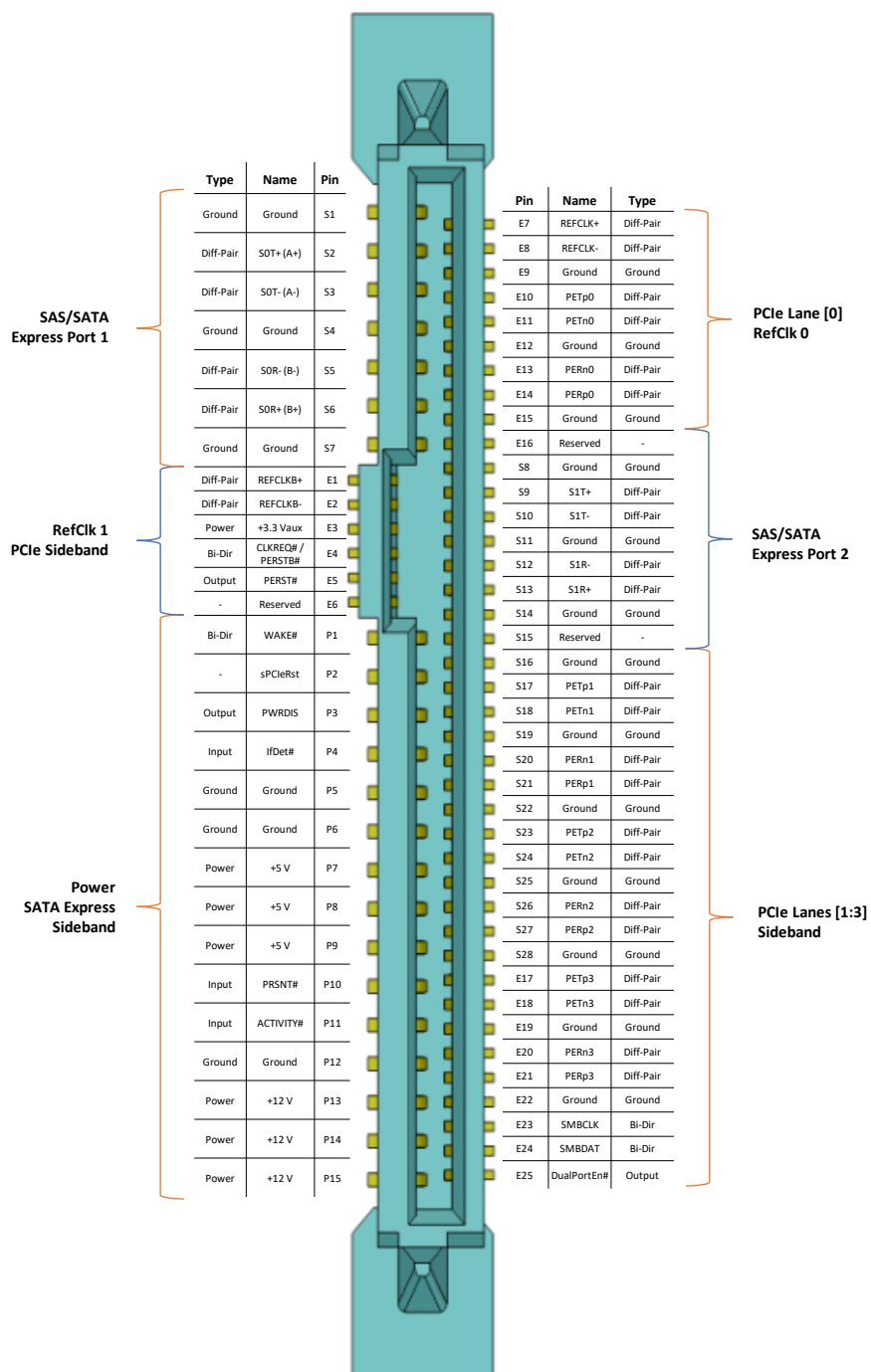
#### Note

***If you are unsure about hot-plug support on your congatec module, contact your FAE.***

## 2 Connector Overview

This chapter gives a general overview of the U.2 connector. Sub-section 2.1 highlights the overall U.2 connector while sub-section 2.2 lists the pinout of all supported interfaces.

### 2.1 U.2 Connector Overview



## 2.2 General U.2 Connector Pinout

The table below lists the pinout of interfaces that are supported with U.2.

Please note that for SATA and SATA Express implementations on U.2, the below signal names do not match the SATA specification exactly.

Pin	Name	Type	PCIe NVMe	SATA	SATA Express	SAS x4
P1	WAKE#	Bi-Dir			Optional	
P2	sPCleRst	-				
P3	PWRDIS	Output			Optional	
P4	lfdet#	Input	Optional		Optional	Optional
P5	Ground	Ground				
P6	Ground	Ground				
P7	+5 V	Power				
P8	+5 V	Power				
P9	+5 V	Power				
P10	PRSNT#	Input		Optional	Optional	
P11	ACTIVITY#	Input		Optional	Optional	
P12	Ground	Ground		Optional	Optional	
P13	+12 V	Power				
P14	+12 V	Power				
P15	+12 V	Power				
S1	Ground	Ground				
S2	S0T+ (A+)	Diff-Pair				
S3	S0T- (A-)	Diff-Pair				
S4	Ground	Ground				
S5	S0R- (B-)	Diff-Pair				
S6	S0R+ (B+)	Diff-Pair				
S7	Ground	Ground				
S8	Ground	Ground				
S9	S1T+	Diff-Pair				
S10	S1T-	Diff-Pair				

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<b>S11</b>	Ground	Ground				
<b>S12</b>	S1R-	Diff-Pair				
<b>S13</b>	S1R+	Diff-Pair				
<b>S14</b>	Ground	Ground				
<b>S15</b>	Reserved	-				
<b>S16</b>	Ground	Ground				
<b>S17</b>	PETp1	Diff-Pair				
<b>S18</b>	PETn1	Diff-Pair				
<b>S19</b>	Ground	Ground				
<b>S20</b>	PERn1	Diff-Pair				
<b>S21</b>	PERp1	Diff-Pair				
<b>S22</b>	Ground	Ground				
<b>S23</b>	PETp2	Diff-Pair				
<b>S24</b>	PETn2	Diff-Pair				
<b>S25</b>	Ground	Ground				
<b>S26</b>	PERn2	Diff-Pair				
<b>S27</b>	PERp2	Diff-Pair				
<b>S28</b>	Ground	Ground				
<b>E1</b>	REFCLKB+	Diff-Pair	DualPort Only			
<b>E2</b>	REFCLKB-	Diff-Pair	DualPort Only			
<b>E3</b>	+3.3 Vaux	Power	Optional			
<b>E4</b>	CLKREQ#/ PERSTB#	CLKREQ# is SSD OD-Output				
<b>E5</b>	PERST#	Output				
<b>E6</b>	Reserved	-				
<b>E7</b>	REFCLK+	Diff-Pair			Optional	
<b>E8</b>	REFCLK-	Diff-Pair			Optional	
<b>E9</b>	Ground	Ground			Optional	
<b>E10</b>	PETp0	Diff-Pair				
<b>E11</b>	PETn0	Diff-Pair				
<b>E12</b>	Ground	Ground				

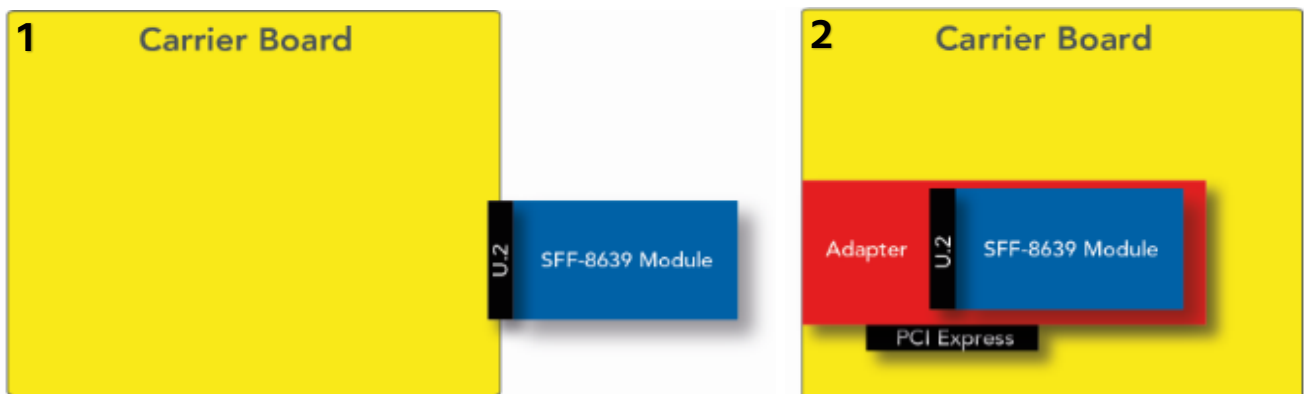


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<b>E13</b>	PERn0	Diff-Pair				
<b>E14</b>	PERp0	Diff-Pair				
<b>E15</b>	Ground	Ground				
<b>E16</b>	Reserved	-				
<b>E17</b>	PETp3	Diff-Pair				
<b>E18</b>	PETn3	Diff-Pair				
<b>E19</b>	Ground	Ground				
<b>E20</b>	PERn3	Diff-Pair				
<b>E21</b>	PERp3	Diff-Pair				
<b>E22</b>	Ground	Ground				
<b>E23</b>	SMBCLK	Bi-Dir	Optional			
<b>E24</b>	SMBDAT	Bi-Dir	Optional			
<b>E25</b>	DualPortEn#	Output	DualPort Only			

## 3 Topologies

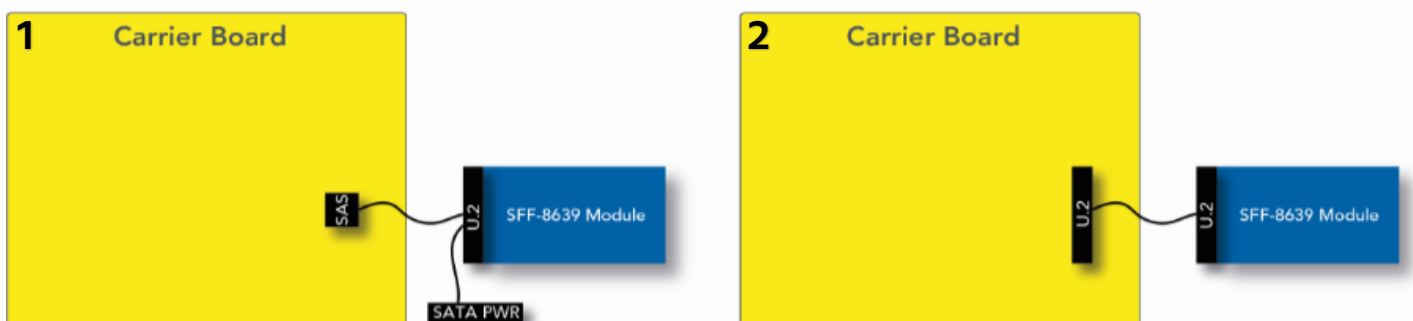
### 3.1 Direct Connect Topologies



Topology 1 (left) connects a U.2 module directly to the standard connector. In this topology, SATA, SAS and U.2 NVMe modules can be connected if the carrier board connector supports all U.2 interfaces.

Topology 2 (right) uses a PCI Express to U.2 adapter to connect a U.2 NVMe module. This topology can be useful for testing SFF-8639 modules on existing carrier boards that have PCIe slots. The adapters currently on the market feature a PCIe x4 lane configuration to utilize the full capacity of the device.

### 3.2 Topologies with Cabling

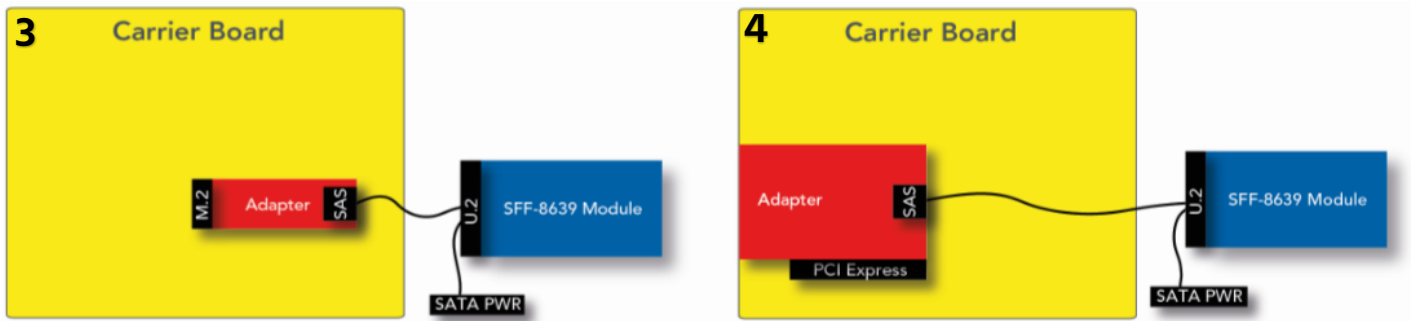


Cabling sets on the market right now focus on reusing existing SFF-8643 cables and connectors. These Mini-SAS HD cables are capable of carrying PCIe signals necessary for operating U.2 NVMe modules. Topology 1 depicts this setup. The carrier board features the SFF-8643 connector. An SFF-8643 to U.2 cable is used to connect the device. In addition, power is supplied to the U.2 module via a SATA power connector.

The second topology depicts a direct connection via U.2 cable.

 Note

*Direct-connect U.2 cables are currently in development.*



Topologies 3 and 4 use the HD Mini-SAS to U.2 cable as described in the first topology. Both setups can be used to determine the function of your U.2 module in existing carriers by using adapters on existing interfaces.

Adapters exist for M.2 as well as PCI Express interfaces.

 Note

*Cabling and adapter solutions can negatively impact signal quality.*